

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

KRISHNA *et al.*

Appl. No.: 09/610,722

Filed: July 6, 2000

For: **Classification Engine in a
Cryptography Acceleration Chip**

Confirmation No.: 5437

Art Unit: 2493

Examiner: COLIN, Carl G.

Atty. Docket: 1875.4310002

Submission of Corrected Drawings

Mail Stop Amendment

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Sir:

In compliance with the request for Corrected Drawings, submitted herewith are nine (9) sheets of Replacement drawings containing Figures 1A, 1B, 2-5, 6A, 6B, and 7 corresponding to the above-captioned application. Identification of the drawings is provided in accordance with 37 C.F.R. § 1.84(c). Acknowledgment of the receipt, approval, and entry of these drawing(s) into this application is respectfully requested.

It is not believed that an extension of time is required, other than any already provided herewith. However, if an extension of time is needed to prevent abandonment of the application, then such extension of time is hereby petitioned. The U.S. Patent and Trademark Office is hereby authorized to charge any fee deficiency, or credit any overpayment, to our Deposit Account No. 19-0036.

Respectfully submitted,

STERNE, KESSLER, GOLDSTEIN & FOX P.L.L.C.



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